

< *New market survey report* >

FO-WLP & Encapsulant Report

~ Markets and Technologies ~

Japan Marketing Survey Co., Ltd

<http://www.jms21.co.jp/>

TEL:81-3-5641-2871 FAX:81-3-5641-0528

3-10-14 Higashi-Nihonbashi Chuoku Tokyo 103-0004 Japan

Focal points of survey

▼ FO-WLP

1. Driver of Market expansion

- Application IC: AP, BB/RF, PMIC, Substitute PKG of 2.5D IC,
- Assembly base format: wafer base or panel base

2. Technical issues for cost reduction and market expansion

- Shift from wafer base assembly to panel base assembly, Enlargement of assembly work size
- Technologies and issues to realize panel based assembly; warpage, mounting accuracy, tact, apply of resins for panel base...

▼ Encapsulation and material

1. Market trends of encapsulants for FO-WLP

- Promise of liquid, granular and sheet types
- By usage: for FO-WLP, or for Other packages
- for FO-WLP by assembly base format, by application IC

2. Technical trends of encapsulation for FO-WLP:

- Warpage, application for panel base format, dust, die shift, non-filling...

Subjects of survey

<Subjects of survey>

◆ FO-WLP (Fan-out Wafer Level Package):

- Chip-First type, RDL-First type
- Face-down type, Face-up type
- Wafer base assembly, Panel base assembly

* But RDL-first type FO-WLP not using photolithography for semiconductor is not included.

◆ Encapsulation system and material:

- System: Compression Mold, Vacuum printing, Lamination, Others
- Material: Liquid type, Granular type, Sheet type, Tablet type
- For FO-WLP, for others (MUF, PKF for long wires, hollow encapsulation, etc.)

<Companies surveyed>

▼ FO-WLP assembler

- ASE, SPIL, TSMC, Amkor, STATS ChipPAC, Nanium, Nepes, Deca, J Devices, Infineon, Freescale, Toshiba, Fujitsu laboratories, Others

▼ Encapsulant material supplier and equipment manufacturer

- Nagase Chemtex, Panasonic, Ajinomoto FT, Sumitomo Bakelite, Hitachi Chemical, Kyocera, Shin-Etsu Chemical, Nitto Denko, Others
- Apic Yamada, TOWA, Others

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Samples of contents (from Chapter 1)

1.1 Overview of FO-WLP market trend

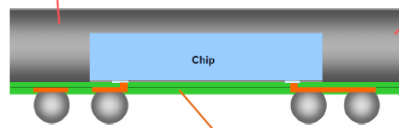
Market size forecast	<ul style="list-style-type: none"> Our estimation for the market size of FO-WLP for AP is based on the amount of smartphone units. CAGR for 5 years starting from 2015 is 8.0% on a unit base and 8.0% on an amount base.
Major applications (IC)	<ul style="list-style-type: none"> In 2015 the major applications were Baseband (BB) and Power Management IC (PMIC). In and after 2016 applications are measured by unit quantity and sales amount.
Market trend	<ul style="list-style-type: none"> The adoption of FO-WLP is expected to increase. Panel based package assembly. IC for high-end systems. Wafer-based assembly area. (By our forecast, the area will expand.) It is anticipated that the market size will increase.
FO-WLP assembler's status	<ul style="list-style-type: none"> In production: STATS ChipPAC, ASE, etc. Under Development: etc.

5.1 Major suppliers of materials and equipments related to FO-WLP

Wafer reconstitution process (encapsulation)

Temporary adhesive material:
Nitto Denko, ---, ---, ---,
---, ---, ---,
---, ---, Others

Liquid: Nagase Chemtex, ---, ---,
Others



CM: Apic Yamada, ---, ---, Others

Mold Release Film: ---, Others

Granule: ---, ---,
---, ---, Others
Sheet: Nitto Denko, ---, ---,
---, ---, Others

RDL formation process

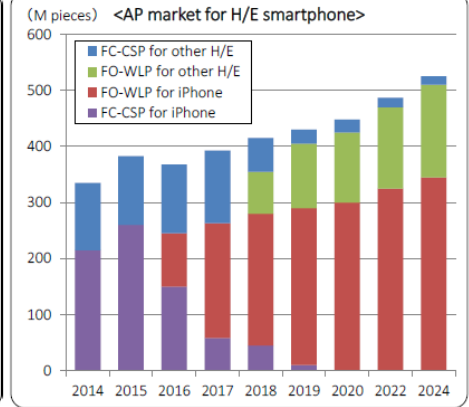
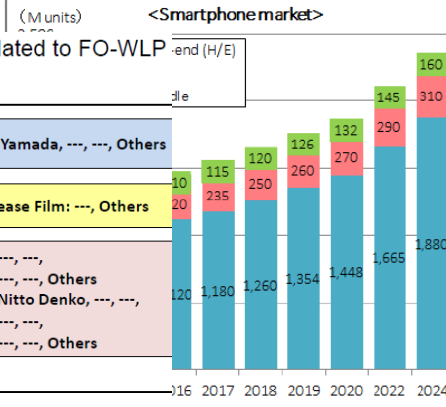
Dielectric material for RDL:
---, ---,
---, ---, Others

Exposure: ---, ---, ---,
---, Others

LDI: ---, ---, ---, Others

2.2 Market size forecast of FO-WLP for AP

【Market size forecast of smartphone by class (Left) and Market size forecast of AP for High-end smartphone by PKG type (Right)】



Market size forecast of AP packaged by FO-WLP technology, classified by application

	2014	2015	2016	2017	2018	2019	2020	2022	2024
Edge	0	0	95						
Core Smartphone	0	0	0	0					
Assistive	0	0							

2.4 Related market and cost comparison of IC packages used for AP

【Market and cost comparison of Apple A10 and A8】

A series		A10 (iPhone 7)	A8 (iPhone 6)
PKG name		InFO-WLP (FO-WLP)	FC-CSP
PKG market size	Quantity	--- M pieces*1	--- M pieces
	Amount		
	Unit cost (a)		
Market size of Encapsulant for A series	Quantity		
	Amount		
	Unit cost		
Dielectric material market	Quantity		
	Amount		
	Unit cost		
Unit cost of material (b)	Encapsulant [ratio (b/a)]		
	RDL material [ration (b/a)]		

2.5 Technology roadmap

【Technology roadmap of AP for High-end smartphone】

				Year	2014	2016	2018	2020	2022	2024
AP Market for High-end smartphone *1					335					
PKG	Whole	Structure	PoP	⇒						
	Top (Memory)	Chip type	LPDDR series	3						
		Wide IO type								
Bottom (AP)	PKG type	FC-CSP	100%							
		FO-WLP	0%							
FO-WLP for AP		Chip combination	Single chip	100%						
			+ α (Multi)	0%						
RDL of FO-WLP		Single side	3-layer	-						
		Double side	4-L & more	-						
Encapsulant	Thickness	[μm]		-						
		Material type	Liquid		-					
			Granule		-					
		Sheet		-						
FO-WLP assembly base			Wafer base	-						

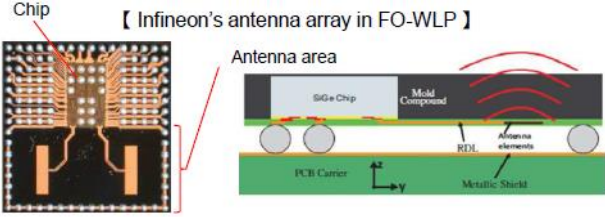
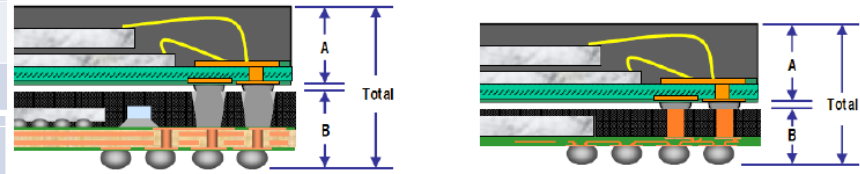
Samples of contents (from Chapter 2)

【Types of FO-WLP and the comparisons】

【 Performance comparison of FC bonders and mounters 】

Process type	Encapsulated base		PCB-technology	RD	FC bonder		Mounter	
	Face-down mounting	Face-up mounting			For CoW	For large size	Wafer supply type *1	Tray part/Reel part supply*1
Work size					~ 300 mm wafer	~ 560 x 610 mm	~ 610 x 610 mm	~ 710 x 774 mm
Alignment accuracy								
Tact								
Chip orientation								
Si wafer								
Interconnection*2	Bump							
	FC connection							
Representative FO-WLP name								

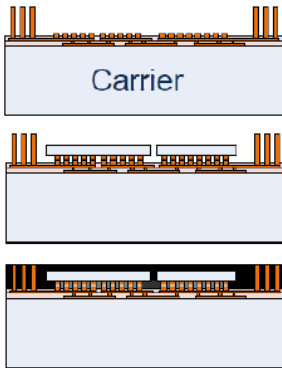
【Comparison of PKG height of AP by type (Left: FC-CSP based PoP, right: FO-WLP based PoP)】



【 Comparison of compatibility to the panel forms, in view of the application and flow of resin, classified by forms of encapsulant】

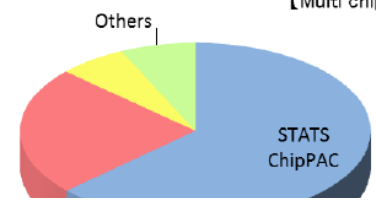
Form of encapsulant	Liquid		Granular	Sheet	
Encapsulation system	CM	Printing	CM	CM	Lamination
Resin	Single or multi-dot, Drawing	One side*1	Whole surface	Whole surface	Whole surface
Application position	Long	Very short	Very short	Very short	Very short
Flow distance*2	Long	Very short	Very short	Very short	Very short
Can be used	Available	Available	Available	Available	Available

【Comparison of mold through via process by type】



【Assembling flow of FO-WLP in case of Encapsulated base type with face-up mounting: M-series】

Chip wafer process	Reconstitution process	RDL formation process
1. Bumping on chip wafer	2. Adhesive sheet lamination & Chip mounting	5. Die location measurement & Dielectric coating
3. Encapsulation & Carrier de-bonding	3. Encapsulation & Carrier de-bonding	6. Pad part opening
4. Topside gridding	4. Topside gridding	



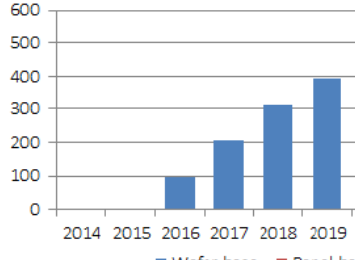
6.2 List of FO-WLP assemblers' status by assembly work size

Assembler	Name of FO-WLP	Wafer		Panel	
		Status	Work size	Status	Work size
ADL Engineering	pWLB				
Amkor Technology	WLFO				
	SWIFT/SLIM				
ASE	aWLP (M-series)				
Deca Technologies	M-series				
Freescal Semiconductor	RCP				
Infineon Technologies	eWLB				
J-Devices	PLP				

【 Market size forecast of FO-WLP by assembly work base and status】

Wafer base	Application	2014	2015	2016	2017	2018
		RF/PM/Analog				
BB						
AP			0			
mm Wave Radar						
CPU/GPU/FPGA			0			
IC for Health/Others						
Total (K wafer)			259			

【 Market size forecast of FO-WLP】



Samples of contents (from Chapter 3)

【 Types of encapsulation system 】

【 List of suppliers of semiconductor encapsulants by usage 】

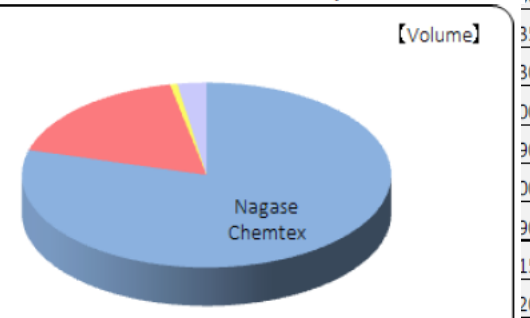
	Schematic views	Process outline	Type
Transfer Mold (TM)		Molding is conducted by filling the molding dies with resin, plunged through gates into the cramped dies.	Tablet Liquid
Compression Mold (CM)		Molding is conducted by compression, made by cramping the dies with resin applied to the internal of dies prior to the cramping.	Granule Liquid Sheet
Vacuum Printing		Filling is made by squeezing down the resin with a squeegee, while the form is arranged by a mask.	Liquid
Vacuum Lamination		Molding is conducted by compression, after the resin is laminated by vacuum.	

	Wafer / Panel base encapsulation			For substrate manufacture		Underfilling as well as Overmolding	
	FI-WLP	FO-WLP	2.5D	EAD/EPD*1	MIS*2	MUF	Hollow
Ajinomoto Fine-Techno							
Hitachi Chemical							
Kyocera (Chemical)							
Nagase Chemtex							
Namics							
Nitto Denko							
Panasonic							
Sanyu Rec							

【 Sales status of major encapsulant manufacturers by form of material (2015) 】

Manufacturer	Amount (M JPY)				
	Tablet	Granule	Liquid	Sheet	Total
Sumitomo Bakelite
Hitachi Chemical
Panasonic
Kyocera
Shin-Etsu Chemical

【 Manufacturers' share of FO-WLP encapsulant market in 2015 (Volume) 】



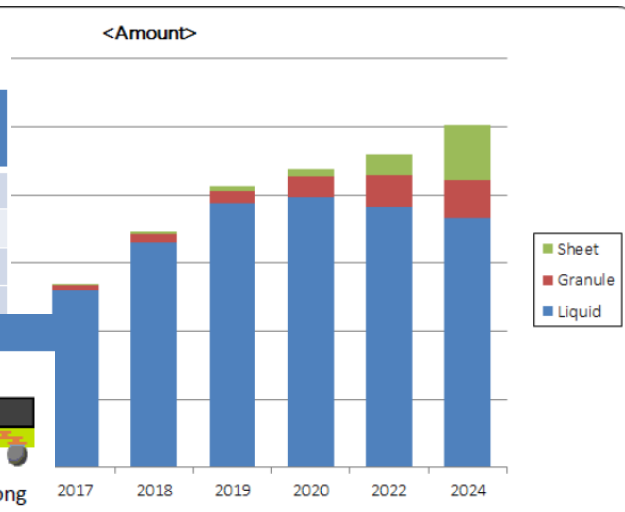
【 Characteristics of granule encapsulants 】

Encapsulant company	Product	Type	Filler size (µm)	Cut size (µm)
		Granule	55	54
		Granule	20 ~ 74	

Introduction of panel base



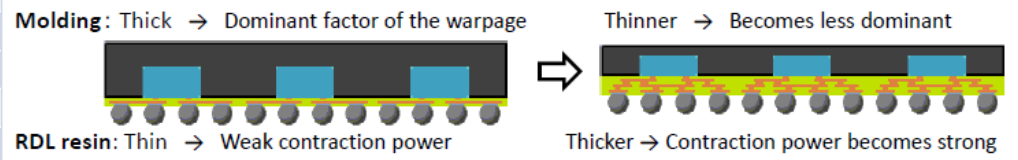
【 Market size forecast of FO-WLP encapsulant by form of material (in amount) 】



【 Comparison of characteristics of encapsulants by form 】

Property	Solid		Liquid *2	Sheet *3
	Tablet	Granule/Powder		
Spiral flow				
Tg				
CTE	Tg	115 ~ 175 deg.C	45 ~ 175 deg.C	110 ~ 190 deg.C
	Gel time *2	20 ~ 45 s	60 ~ 90 s	60 ~ 90 s
Modulus	Viscosity	175deg.C	5 ~ 15 Pa·s	7 ~ 15 Pa·s
	25 deg.C	-	-	15 ~ 800 Pa·s
Flexural modulus				
Application				

Thinner encapsulation layer / shift to multilayer RDL



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